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ADRC-based Model Predictive Current Control for PMSMs fed by Three-phase Four-switch Inverters

Topics: Motor Drives and Motor Control

Abstract—An automatic disturbances rejection control (ADRC)-based model predictive current control (MPCC) strategy is developed for permanent magnet synchronous motors (PMSMs) fed by three-phase four-switch inverters. The model of a PMSM fed by a three-phase four-switch inverter is built firstly. Then the ADRC and MPCC are respectively designed. The resultant ADRC-based MPCC PMSM drive has fault-tolerant effective. On the other hand, compared with PI-based MPCC PMSM drive, it possesses better dynamical response behavior and stronger robustness in the presence of variation of load torque. The simulation results validate the feasibility and effectiveness of the proposed scheme.

I. Introduction

One of the most common types of potential failures in an electrical drive system is the breakdown of one transistor in the voltage source inverter (VSI), which may cause unacceptably high pulsating torque and consequently the drive system has to be interrupted. To solve the problem, a common method is to design a fault-tolerant inverter topology plus an effective control strategy that is able to manage the drive system during the inverter fault. Over the past years, several reconfigurations of inverter topologies for overcoming its faults have been developed. One of them is named as extra-leg split capacitor scheme[1]. This topology enables the electrical machine to still operate in a three-phase mode during the fault and thus is referred to as three-phase four-switch mode which is employed in this paper.

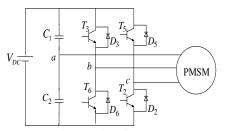
For permanent magnet synchronous motor (PMSM) drive system, model predictive current control (MPCC) [2] is an emerging control strategy. Its main objective is to control instantaneous stator current with high accuracy in a transient interval as short as possible and thus plays an important role to ensure the quality of the torque and speed control. MPCC adopts the principle of model predictive control (MPC) [3-4] and can provide higher dynamic performance and lower stator current harmonic.

For a conventional PI-based MPCC PMSM drive system, its speed regulator employs the algorithm of PI. In general, PI may perform well under certain operating conditions, but degrade dynamic performance under other operating conditions when disturbances arise. To improve the robustness of PI, several techniques have been proposed in recent years. In these techniques, automatic disturbances rejection control (ADRC) is an effective and practical one, which was firstly proposed by Han [5]. The key of ADRC is to reformulate the problem by lumping various known and unknown quantities that affect the system performance into total disturbance [6]. And the total disturbance can be actively estimated and then rejected. Due to being independent of the accurate model of the system, it is very robust against disturbances. For this reason, we propose replacement of PI with ADRC in our research.

For three-phase four-switch inverter in PMSM drive systems, in order to improve dynamic performance and system robustness, a novel ADRC-based MPCC strategy is proposed in our research.

II. DYNAMIC MODEL OF PMSMs FED BY THREE-PHASE FOUR-SWITCH INVERTER

As for three-phase PMSM drive, assume that one transistor is broken down in the inverter leg corresponding phase a. In this case, we adopt the three-phase four-switch inverter as shown in Fig.1.



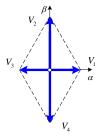


Fig.1 Three-phase four-switch inverter and PMSM drive

Fig.2 The layout of voltage space vectors

As for Fig.1, three-phase stator voltage in *abc*-system is given by:

$$\begin{cases} u_a = V_{DC} (1 - S_b - S_c)/3 \\ u_b = V_{DC} (-0.5 + 2S_b - S_c)/3 \\ u_c = V_{DC} (-0.5 - S_b + 2S_c)/3 \end{cases}$$
 (1)

where u_a , u_b and u_c are stator voltages, V_{DC} is DC bus voltage, and S_i (i= b, c) the upper switch state of one of two legs. S_i = 1 or S_i = 0 when upper switch is on or off as shown in Fig. 1. The combination of S_b , S_c may form four switching states corresponding four voltages V_1 , V_2 , V_3 , V_4 as shown in Fig. 2.

Consider surface-mounted PMSM. The mathematical model in $\alpha\beta$ -system is expressed as follows

$$\begin{cases} \dot{i}_{\alpha} = \left(-R_{s}i_{\alpha} + \psi_{f}\omega_{r}\sin\left(p\theta_{r}\right) + u_{\alpha}\right)/L \\ \dot{i}_{\beta} = \left(-R_{s}i_{\beta} - \psi_{f}\omega_{r}\cos\left(p\theta_{r}\right) + u_{\beta}\right)/L \end{cases}$$
(2)

where i_{α}, i_{β} , u_{α}, u_{β} , ψ_{α} , ψ_{β} are α and β axis stator current, stator voltage, and stator flux, respectively. L is the stator-winding inductance, R_s the stator winding resistance. ψ_f the permanent magnet flux, and θ_r and θ_r are the measured rotor angular displacement and speed, respectively.

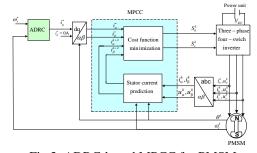
By Newton's law, the electromagnetic torque equation can be written as

$$J\dot{\omega}_r = T_r - T_l - B_m \omega_r - T_f \tag{3}$$

where J, T_e , T_l , B_m and T_f are the inertia of moment, electromagnetic torque, exogenous load torque, viscous friction coefficient and coulomb friction torque, respectively and p is the number of pole pairs.

III. DESIGN OF ADRC-BASED MPCC FOR PMSM DRIVE SYSTEMS FED BY THREE-PHASE FOUR-SWITCH INVERTERS

The diagram of ADRC-based MPCC system proposed is shown in Fig.3.



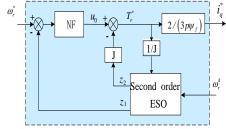


Fig.3 ADRC-based MPCC for PMSM

Fig.4 Diagram of ADRC

A. ADRC design

Fig. 4 shows the diagram of ADRC, which consists of an extended state observer (ESO) and a nonlinear function (NF). The ESO is used to estimate the unmeasured state and the real action of the unknown disturbances to build a solid base for better performance and disturbances compensation. The NF is used to synthesize the control action.

Let
$$x_1 = \omega_r, \ y = x_1 \tag{4}$$

The state space equation of (3) is yielded as following

$$\begin{cases} \dot{x}_1 = -B_m x_1 / J - \left(T_I + T_f\right) / J + T_e / J \\ y = x_1 \end{cases}$$
(5)

And the total disturbance f(t) is defined as

$$f(t) = -B_m \omega_r / J - \left(T_l + T_f\right) / J \tag{6}$$

Treating f(t) as an augmented variable x_2 , i.e., $x_2 = f(t)$. and letting $\dot{f}(t) = g(t)$, with g(t) unknown, one can rewrite the state space expression in (5) as

$$\begin{cases} \dot{x}_1 = x_2 + T_e/J \\ \dot{x}_2 = g(t) \end{cases}$$
 (7)

Now we construct a second-order state observer, denoted as the ESO, in the form of

$$\begin{cases} e = \omega_r - z_1 \\ \dot{z}_1 = z_2 - \beta_1 fal(e, a_1, \delta_1) + T_e/J \\ \dot{z}_2 = -\beta_2 fal(e, a_2, \delta_2) \end{cases}$$

$$(8)$$

where β_1 , β_2 , α_1 , α_2 and δ_1 are positive parameters, $fal(x,a,\delta)$ is a nonlinear function defined as

$$fal(x,a,\delta) = \begin{cases} x \cdot \delta^{a-1}, & |x| \le \delta \\ sign(x) \cdot |x|^a, & |x| > \delta \end{cases}$$
(9)

By canceling the influence of total disturbance f(t) using z_2 , ADRC actively compensates for f(t) in real time. The reference torque in Fig.4 is designed as following,

$$T_{e}^{*} = u_{0}(t) - Jz_{2} \tag{10}$$

where $u_0(t)$ is the output of NF defined as

$$u_0(t) = \beta_3 fal\left(\omega_r^* - z_1, a_2, \delta_3\right) \tag{11}$$

B. Model predictive current control

For conventional MPCC, the minimum cost function is such chosen that both i_{α} and i_{β} are as close as possible to their reference values. Its definition is as follows

$$\min \{g_i\} = |i_{\alpha}^* - i_{\alpha}^{k+1}| + |i_{\beta}^* - i_{\beta}^{k+1}|
st.V_i \in \{V_1 \quad V_2 \quad V_3 \quad V_4\}, i = 1, \dots, 4$$
(12)

where the current predictions i_{α}^{k+1} and i_{β}^{k+1} at (k+1)th instant can be expressed as the following

$$\begin{cases} i_{\alpha}^{k+1} = i_{\alpha}^{k} + T_{s} \left(-R_{s} i_{\alpha}^{k} + \psi_{f} \omega_{r}^{k} \sin\left(p\theta_{r}^{k}\right) + u_{\alpha}^{k} \right) / L \\ i_{\beta}^{k+1} = i_{\beta}^{k} + T_{s} \left(-R_{s} i_{\beta}^{k} - \psi_{f} \omega_{r}^{k} \cos\left(p\theta_{r}^{k}\right) + u_{\beta}^{k} \right) / L \end{cases}$$

$$(13)$$

To compensate for the computation delay, the cost function in (12) is changed to (14) as below

$$\min_{\{g_i\}} = \begin{vmatrix} i_{\alpha}^* - i_{\alpha}^{k+2} \end{vmatrix} + \begin{vmatrix} i_{\beta}^* - i_{\beta}^{k+2} \end{vmatrix}$$

$$s.t.V_i \in \{V_1 \quad V_2 \quad V_3 \quad V_4\}, i = 1, \dots, 4$$
(14)

IV. SIMULATION AND ANALYSIS

The designed control system in Fig.3 is implemented in Matlab. The parameters of PMSM: R_s is 2.875Ω , L is 0.0085H, ψ_f is 0.175Wb, p is 1, V_{DC} 350V, n_N is 3000rpm, J is 0.0008Kg.m², T_n is 3N.m, T_f is 0 and B_m is 0.001Nms.

A. Fault-tolerant effect

Figs. 5 and 6 show their comparison results. Comparing Fig. 5 with Fig. 6, it can be seen that, for the

PMSM system fed by unhealthy inverter, it has fault-tolerant effective and its speed and torque could be regulated in a satisfactory manner and its performance is almost as good as the PMSM system fed by healthy inverter.

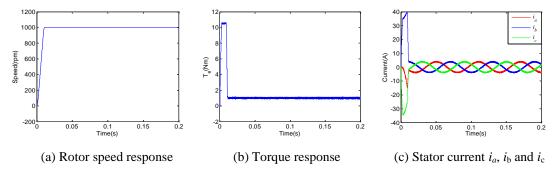


Fig.5 Dynamic response for PMSM fed by three-phase six-switch inverter (i.e. healthy inverter)

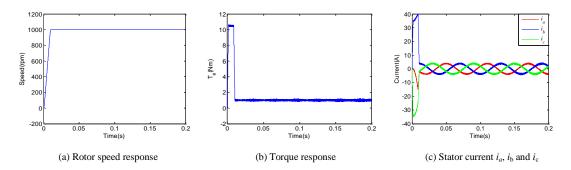


Fig. 6 Dynamic response for PMSM fed by three-phase four-switch inverter (i.e. unhealthy inverter)

B. Robustness

Two systems are compared, which correspond to the PI-based MPCC and ADRC-based MPTC PMSM systems, respectively. Except their distinct speed regulators (i.e. PI and ADRC), two systems have completely identical structure and parameters of MPCC.

1) Comparison of anti-interference of load

To make a fair comparison of both control schemes, the parameters of PI for PI-based MPCC PMSM system are adjusted such that PI-based MPCC system has almost identical overshoot and settling time with ADRC-based one.

Figs.7 and 8 show their dynamical responses. Comparing Fig.7(a) with Fig.8(a), it can be seen that, for ADRC-based MPCC PMSM system, its speed can sharply adapt to the change of external load change in a satisfactory manner, and its capable of accommodating the challenge of load disturbance is superior to PI-based one's.

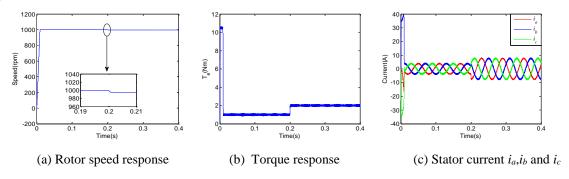


Fig.7 Dynamic response of PI-based MPCC scheme for PMSM fed by an unhealthy inverter

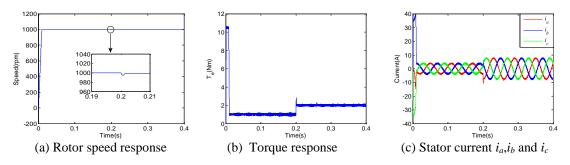


Fig.8 Dynamic response of ADRC-based MPCC scheme for PMSM fed by an unhealthy inverter

2) Comparison of dynamical response

To make a fair comparison, the parameters of PI are adjusted such that PI-based MPCC system has almost identical ability of anti-load disturbance with ADRC-based one. Fig.9 shows their speed responses. From Fig.9, it can be seen that, compared with PI-based MPCC PMSM system, the overshoot and settling time of ADRC-based one are obviously smaller and thus its dynamical response is superior.

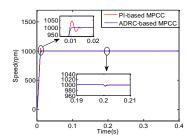


Fig.9 Speed response comparison between PI-based and ADRC-based MPCC schemes

V. CONCLUSION

The designed ADRC-based MPCC PMSM fed by an unhealthy inverter is fault-tolerant effective, and its speed & torque could be regulated in a satisfactory manner. On the other hand, ADRC-based MPCC strategy can guarantee the ability to achieve two objectives simultaneously: satisfactory dynamical response and strong disturbance rejection, whereas PI-based one can guarantee only one of the two objectives.

VI. REFERENCES

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VII. APPENDIX

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